



Invionics Launches Platform that Speeds In-house EDA Tool Development, Solves Critical Design Flow Bottlenecks

New Invio™ platform re-ignites innovation through internal tool development

Vancouver, British Columbia, November 11, 2014 – Invionics, a company providing software to automate the semiconductor design and verification process, today announced Invio, a custom EDA tool platform that provides IC developers with a fast and easy way to create custom applications that significantly improve their IC design flow.

Today's CAD groups and IC designers at semiconductor companies are increasingly building in-house custom EDA tools, flows and scripts that will enable competitive differentiation. The Invio Platform, anchored by the RTL Processing Engine, was developed to take this internal development to a new level. With an easy to learn Tcl or Python API, CAD and design engineers can create robust, high performance EDA design tools and flows. Furthermore they can automate the RTL changes in a way that can be reviewed, audited and archived in a re-usable way. Adding customized automation to this point in the design process often dramatically reduces IC development time and cost.

“The Invio platform was developed through collaboration with some of the industry's leading semiconductor companies, which are facing ever increasing pressures to differentiate their designs in shrinking time to market windows. Using Invio, customers have been able to build in-house EDA tools in a fraction of the time typically required, thereby creating highly customized IC design processes,” said Brad Quinton, CEO and founder of Invionics.

“The Invio Platform is enabling Maxim to build custom EDA applications to extract information from complex SystemVerilog verification environments,” said Prabal Bhattacharya, senior principal MTS, EDA at Maxim Integrated. “We found the Python API to be easy to use and the RTL Processing Engine's language coverage of SystemVerilog to be robust and extensive.”

The Invio Platform from Invionics includes the RTL Processing Engine, the Custom GUI Builder and the Application Packager, and offers a choice of additional modules that speed development in specific areas including the RTL Modification Module, the Netlist Modification Module, Functional Verification Module and the SoC Assembly Module. The platform enables applications that seamlessly integrate with third-party EDA tools

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and flows using industry-standard languages and formats (Verilog, Verilog-AMS, SystemVerilog, VHDL, IP-XACT). The platform can be used to develop tools for any front-end EDA application, from simply 'find all synchronous resets,' to more complex 'SoC Assembly' or 'DFT Insertion' applications.

Availability and Pricing

The Invio EDA Platform is available now from Invionics. Evaluation and pricing are available upon request. For more information, visit the Invionics website located at: www.invionics.com.

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About Invionics

Invionics accelerates IC development and design automation. Leveraging hands-on hardware design experience and proven EDA tool development success, the company provides custom EDA development platforms that deliver immediate value to semiconductor, system and EDA companies. Invionics also provides experienced custom R&D to extend its platforms, enabling design teams to quickly implement key functionality and gain competitive advantage for their products. Corporate headquarters is located in Vancouver, British Columbia, Canada. Telephone: (604) 336-3325. Email: info@invionics.com. Website: www.invionics.com.

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